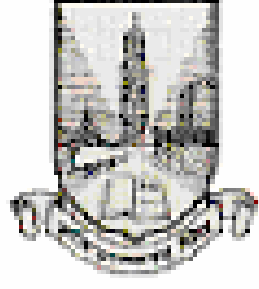


University of Mumbai



706

FIRST HALF 2015

EXAMINATION TIME TABLE
PROGRAMME - M.E. (ELECTRONICS ENGG.)(CBSGS)
SEMESTER - II

Days and Dates	Time	Paper
Wednesday, May 13, 2015	11:00 a.m. to 02:00 p.m.	Power Electronic Divices and Disign.
Tuesday, May 19, 2015	11:00 a.m. to 02:00 p.m.	Advanced Processor Architecture.
Monday, May 25, 2015	11:00 a.m. to 02:00 p.m.	Applications of DSP and IP.
Friday, May 29, 2015	11:00 a.m. to 02:00 p.m.	Elective: III : Advance Networking Technologies.
Friday, May 29, 2015	11:00 a.m. to 02:00 p.m.	Elective: III : Machine Learning.
Friday, May 29, 2015	11:00 a.m. to 02:00 p.m.	Elective: III : Microwave IC.
Friday, May 29, 2015	11:00 a.m. to 02:00 p.m.	Elective: III : Fabrication of Microelectronic IC.
Thursday, June 04, 2015	11:00 a.m. to 02:00 p.m.	Elective: IV : Real Time Operating System.
Thursday, June 04, 2015	11:00 a.m. to 02:00 p.m.	Elective: IV : Modeling and Synthesis with VHDL.
Thursday, June 04, 2015	11:00 a.m. to 02:00 p.m.	Elective: IV : Cryptography and Network Securities.
Thursday, June 04, 2015	11:00 a.m. to 02:00 p.m.	Elective: IV : Application Specific IC Design.

NOTE: The candidates appearing for the examination should report 15 minutes before the start of examination.

Mobile phones and other elctronic gazets are prohibited in the examination hall.

Change if any, in the time table shall be communicated on the university web site.

Mumbai - 400 098

1st April, 2015.

(DINESH BHONDE)

Controller of Examinations.

Ref.No:- MU/Exam/Result/FH/TT/TECH/706/347.