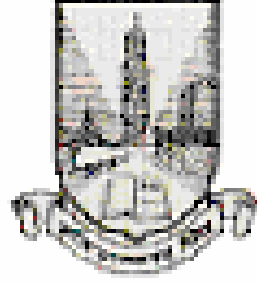


University of Mumbai



626

FIRST HALF 2015

**EXAMINATION TIME TABLE
PROGRAMME - M.E. (ELECTRONICS ENGG.) (CBSGS)
SEMESTER - I**

Days and Dates	Time	Paper
Tuesday, May 12, 2015	11:00 a.m. to 02:00 p.m.	System Modeling and Simulation.
Monday, May 18, 2015	11:00 a.m. to 02:00 p.m.	Modelling of Microelectronic Devices.
Friday, May 22, 2015	11:00 a.m. to 02:00 p.m.	Embedded System.
Thursday, May 28, 2015	11:00 a.m. to 02:00 p.m.	Elective : I. : Advance Digital Communication.
Thursday, May 28, 2015	11:00 a.m. to 02:00 p.m.	Elective : I. :Instrumentation System Design.
Thursday, May 28, 2015	11:00 a.m. to 02:00 p.m.	Elective : I. :Optical Fiber Communication.
Thursday, May 28, 2015	11:00 a.m. to 02:00 p.m.	Elective : I. :Analogue IC Design.
Wednesday, June 03, 2015	11:00 a.m. to 02:00 p.m.	Elective : II. : Wireless and Mobile Networks.
Wednesday, June 03, 2015	11:00 a.m. to 02:00 p.m.	Elective : II. : Advance Digital Image Processing.
Wednesday, June 03, 2015	11:00 a.m. to 02:00 p.m.	Elective : II. : Internetworking Technologies.
Wednesday, June 03, 2015	11:00 a.m. to 02:00 p.m.	Elective : II. : Digital System Design.

NOTE: The candidates appearing for the examination should report 15 minutes before the start of examination.

Mobile phones and other electronic gadgets are prohibited in the examination hall.

Change if any, in the time table shall be communicated on the university web site.

Mumbai - 400 098

28th March, 2015.

(DINESH BHONDE)

Controller of Examinations.

Ref.No:- MU/Exam/Result/FH/TT/TECH/626/305.